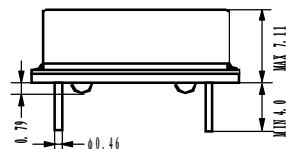
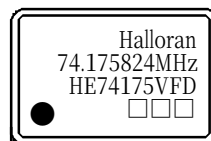


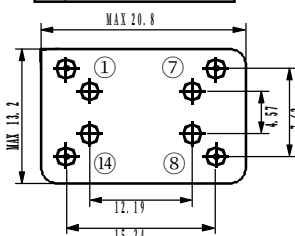
Specification: HE74175VFD		
1	Package	Full Size
2	Type	VCXO
		(No Tristate)
3	Frequency	74.175824 MHz
4	Stability	± 15 ppm max/ $0 \sim +70$ °C
5	Voltage	$+3.3$ v $\pm 5\%$
6	Symmetry	40 % to 60 % (at 1.4 v)
		CMOS (15 pF)
7	Pullability	± 100 ppm min/ 1.65 ± 1.65 v
	Note:	Direct Multiple (not PLL)

外形寸法 Dimensions



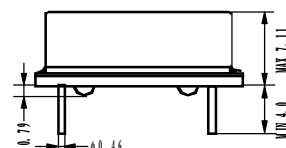
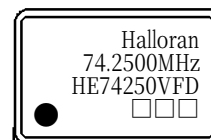
接続端子 Pin Connections

①	V _c
⑦	GND (V _{ss})
⑧	OUTPUT
⑭	+3.3 v (V _{DD})



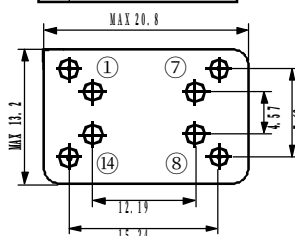
Specification: HE74250VFD		
1	Package	Full Size
2	Type	VCXO
		(No Tristate)
3	Frequency	74.2500 MHz
4	Stability	± 15 ppm max/ $0 \sim +70$ °C
5	Voltage	$+3.3$ v $\pm 5\%$
6	Symmetry	40 % to 60 % (at 1.4 v)
		CMOS (15 pF)
7	Pullability	± 100 ppm min/ 1.65 ± 1.65 v
	Note:	Direct multiple (not PLL)

外形寸法 Dimensions



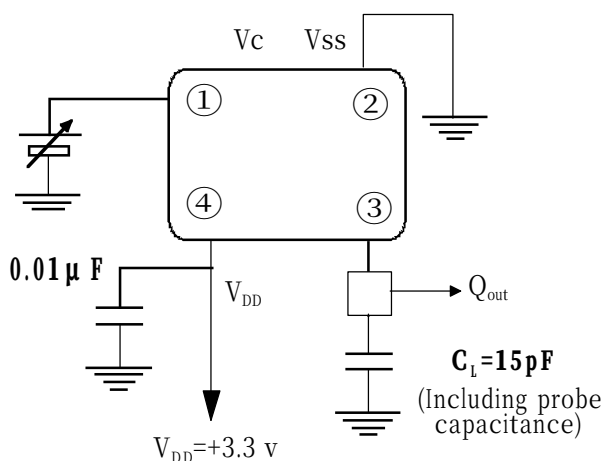
接続端子 Pin Connections

①	V _c
⑦	GND (V _{ss})
⑧	OUTPUT
⑭	+3.3 v (V _{DD})



CMOS出力

V_{DD}=+3.3 v
C_L=15 pF



接続端子 Pin Connections

①	V _c
②	GND (V _{ss})
③	OUTPUT
④	V _{DD} :+3.3 v

出力波形

