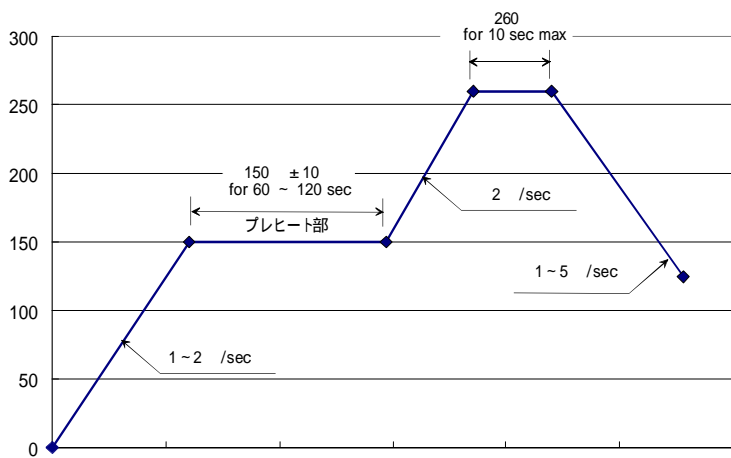
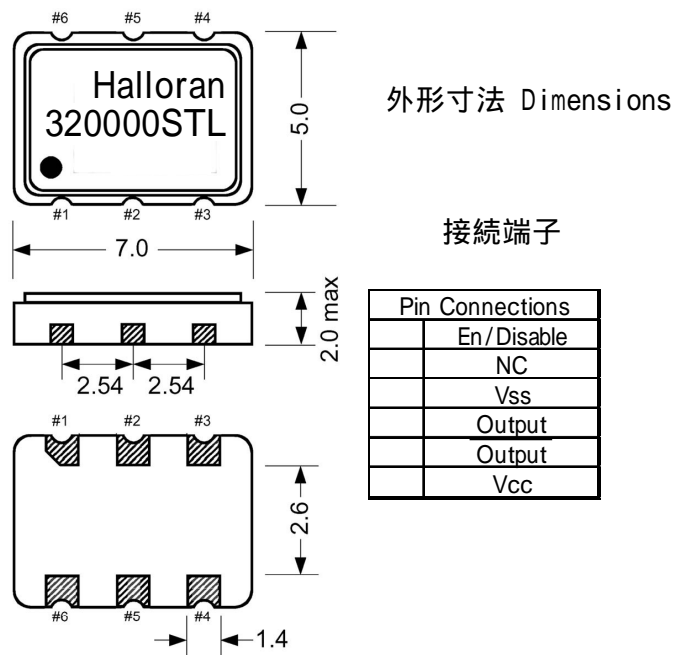
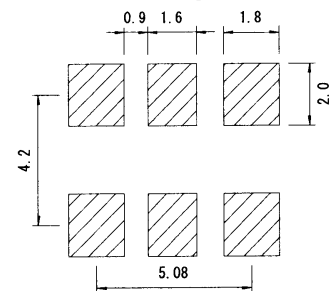


Specification: 320000STL		
1	Package	SMD (7 mm x 5 mm x 1.7 mm)
2	Type	SPXO (With Tristate)
3	Frequency	320.0000 MHz
4	Stability	±50 ppm max/-5 ~ +70
5	Voltage	+2.5 v ± 5 %
6	Current	66 mA max
7	Output Signal	LVDS
	V _{OH}	1.43 v (Typ)
	V _{OL}	1.10 v (Typ)
8	Output load	100 (Offset 1.25 v Typ)
9	Rise and Fall	0.7 nsec max
10	Symmetry	45% to 55%
11	Jitter	0.3 ps typ
	Note:	RoHS Compliant



Test Circuit

Land Pattern Layout (Example)



input pin #1	output pin #4 #5
open or 70 % V _{CC} min	RF-signal
GND or 30 % V _{CC} max	HI-Z
Enable Phase Delay time 2 ms max	
Disable Phase Delay Time 200 ns max	

Output Waveform

