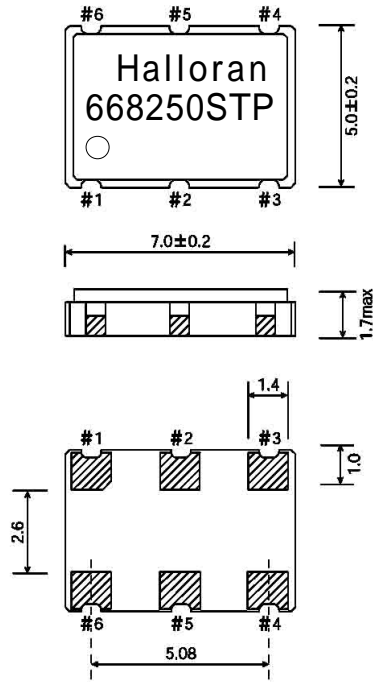


Specification: HE668250STP		
1	Package	SMD (7 mm x 5 mm x 1.7 mm)
2	Type	SPXO (With Tristate)
3	Frequency	668.2500 MHz
4	Stability	±50 ppm max/0 ~ +70
5	Voltage	+3.3 v ± 5 %
6	Current	100 mA max
7	Output Signal	LV-PECL
	V <sub>OH</sub>	2.275 v min
	V <sub>OL</sub>	1.680 v max
8	Output load	50 (V <sub>CC</sub> -2.0 v)
9	Rise and Fall	400 psec max
10	Symmetry	45% to 55%
11	Integrated Jitter RMS (12 kHz to 20 MHz Band)	1.6 ps typ
	Note:	RoHS Compliant

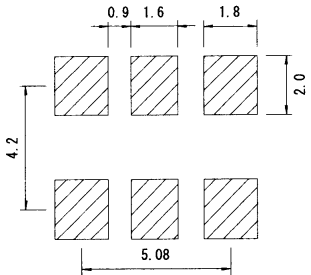


外形寸法 Dimensions

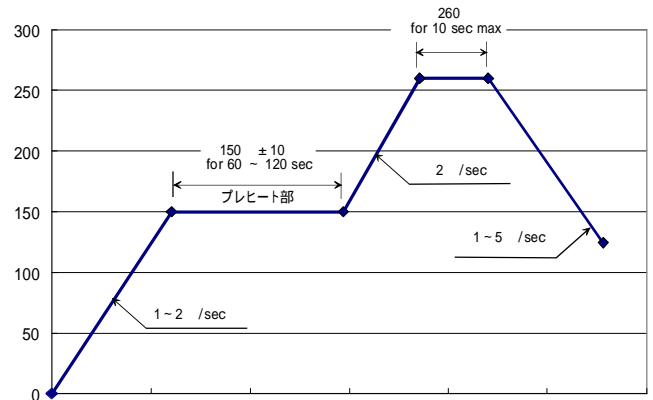
接続端子

Pin Connections	
	En/Disable
	NC
	V <sub>SS</sub>
	Output
	Output
	V <sub>CC</sub>

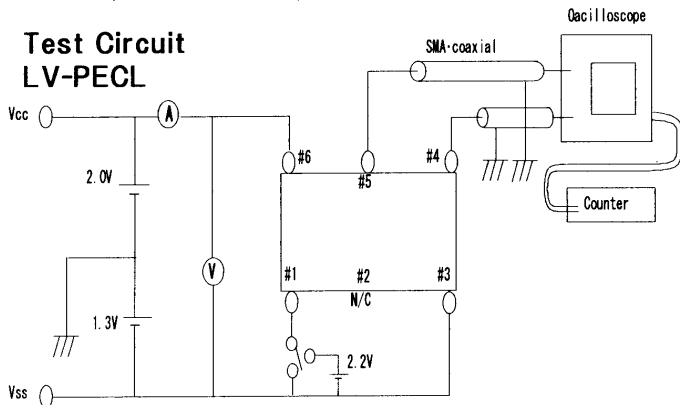
Land Pattern Layout (Example)



Enable-Disable	
input pin #1	output pin #4 #5
H or open	RF-signal
2.2V min	
L	High Z
0.4V max	
Enable Delay Time 100ns max	
Disable Delay Time 100ns max	



Test Circuit  
LV-PECL



Output Waveform

