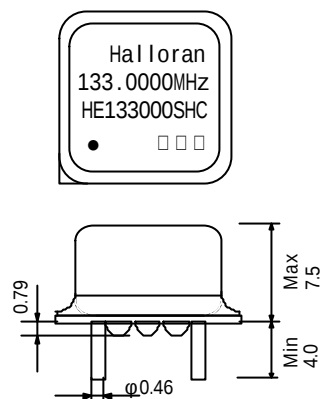
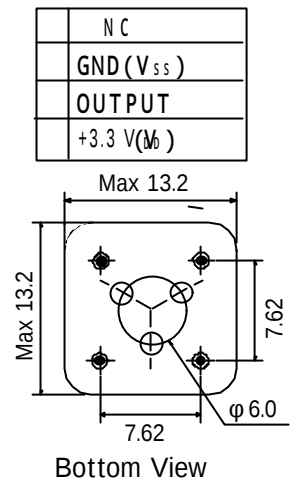


Specification: HE133000SHC		
1	Package	Half Size
2	Type	SPXO
		(No Tristate)
3	Frequency	133.0000 MHz
4	Stability	± 100 ppm max/ -20 ~ +70
5	Voltage	+3.3 v $\pm$ 10 %
6	Symmetry	40 % to 60 % (at 1/2 V _{DD})
		CMOS (15 pF)
7	Pullability	Not Applicable
	Note:	PLL

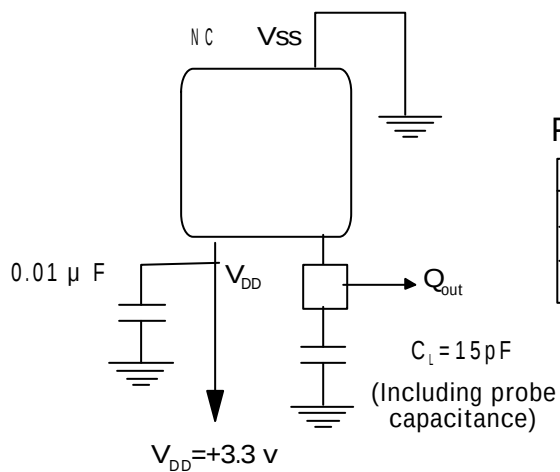
■ 外形寸法 Dimensions



■ 接続端子 Pin Connections



測定回路



■ 接続端子 Pin Connections

NC
V _{SS} : GND
OUTPUT
V _{DD} : +3.3 v

出力波形

